

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L46	1847	clock same (updat\$4 near3 cycl\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 16:19
L44	3	(adjustable) near4 phase near2 (size height) same (clock)	US-PGPUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 15:21
L43	195	(adjustable) near4 phase near2 (size height delay) same (clock)	US-PGPUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 15:21
L42	47	(adjustable) near4 phase same (data near4 rate)	US-PGPUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 15:16
L41	2	"5,838,749",pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 15:12
L40	5	39 not 38	US-PGPUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 15:10
L39	7	(adjustable) near4 phase same (variable near3 rate)	US-PGPUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 15:10

L38	7	{adjustable} near4 phase near4 (step height size) and (variable near3 rate)	{US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 15:07
L36	67	{stall\$4 hold\$4} near4 (output) with ((up and down) transition) same (clock same phase)	{US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:52
L35	185	{stall\$4 hold\$4} near4 (output) same (clock same phase) same ((up and down) transition)	{US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:52
L34	1018	{stall\$4 hold\$4} near4 (output) same (clock) same ((up and down) transition)	{US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:51
L33	0	((output near4 stall\$4)) same ((phase near2 detect\$4)) and ((jitter\$4 dither\$4)	{US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:49
L32	113	((output near4 stall\$4)) same (clock pll (phase near2 detect\$4)) and ((jitter\$4 dither\$4)	{US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:49
L31	183	((output near4 stall\$4)) and (clock pll (phase near2 detect\$4)) and ((jitter\$4 dither\$4)	{US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:49
L30	0	((output near4 stall\$4)) same (clock pll (phase near2 detect\$4)) same ((jitter\$4 dither\$4)	{US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:48
L29	0	((output near4 stall)) same (clock pll (phase near2 detect\$4)) same ((jitter\$4 dither\$4)	{US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:48

L28	0	{{(stage) near4 (output near4 stall)}} same (clock pll (phase near2 detect\$4)) same (jitter\$4 dither\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:48
L27	30	{stall adj logic} same (clock pll (phase near2 detect\$4))	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:39
L26	150	{stall adj logic} and (clock pll (phase near2 detect\$4))	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:39
L25	211	{{(stage) near4 (output near4 hold\$4)}} with (clock pll (phase near2 detect\$4))	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:24
L24	5	{{(stage) near4 (output near4 hold\$4)}} same (clock pll (phase near2 detect\$4)) same (jitter\$4 dither\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:13
L23	356	{{(stage) near4 (output near4 hold\$4)}} same (clock pll (phase near2 detect\$4))	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 14:12
L22	17	{{(flip near2 flop) } with (output near4 hold\$4)} same (clock pll (phase near2 detect\$4)) same (dither jitter)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 13:10
L21	1203	{{(flip near2 flop) } with (output near4 hold\$4)} same (clock pll (phase near2 detect\$4))	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 13:10
L20	1732	{{(flip near2 flop) stage} with (output near4 hold\$4)} same (clock pll (phase near2 detect\$4))	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 13:09

L19	1732	((flip near2 flop) stage) with (output near4 hold\$4) same (clock pll (phase near2 detect\$4))	US-PGRUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 13:09
L18	5909	((flip near2 flop) stage) same (output near4 hold\$4) and (clock pll (phase near2 detect\$4))	US-PGRUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 13:08
L17	13	((flip near2 flop) stage) same (logic near4 hold\$4) same (phase near3 detect\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 13:05
L16	10	((flip near2 flop) same (logic near4 hold\$4) same (phase near3 detect\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 13:05
L15	16	((flip near2 flop) same (hold\$4) same (phase near3 detect\$4) same (dither jitter)	US-PGRUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 12:59
L14	331	((flip near2 flop) same (hold\$4) same (phase near3 detect\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 12:58
L13	968	((flip near2 flop) same (latch\$4 hold\$4) same (phase near3 detect\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 12:58
L12	17	(reduc\$4 near4 jitter) same (hold\$4 stall\$4) same (phase near3 detect\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 12:40
L11	17	(reduc\$4 near4 jitter) same (hold\$4) same (phase near3 detect\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/16 12:40

L10	655	reduc\$4 near4 jitter same (phase near3 detect\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 12:39
L9	8	reduc\$4 near4 dither same (phase near3 detect\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 12:38
L8	84	reduc\$4 near4 dither and (phase near3 detect\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 12:38
L7	4	(stall adj logic) with (hold\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 12:38
L6	1	(stall adj logic) with (hold\$4) and (PLL (phase adj detect\$4))	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 12:38
L5	3	(consecutive near4 (up down)) with hold\$4 same (phase near3 detect\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 12:19
L4	14	(adjust\$4) near4 phase near4 (coarse and fine) same (phase near3 detect\$4) and (data near4 rate)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 12:12
L3	0	(adjust\$4) near4 phase near4 (coarse and fine) same (phase near3 detect\$4) and (variable near4 rate)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 12:12
L2	47	(adjust\$4) near4 phase near4 (coarse and fine) same (phase near3 detect\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 12:11

S238	3	{adjust\$4} near4 phase near4 (coarse and fine) same (phase near3 detect\$4) same pll	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 11:35
S237	98	{adjust\$4} near4 phase near4 (coarse and fine) and (PLL)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 11:34
S236	48	{adjustable} near4 phase near4 (step height size) and (clock)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 11:26
S235	142	{adjustable} near4 phase near4 (step height size)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 11:26
S234	10	{{(variable)} near4 phase near4 (step height size) same (phase near4 adjust\$7) and (clock pll dll cdr)}	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 11:23
S233	681	{{(variable)} near4 phase near4 (step height size)}	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/16 11:23
S232	62	charge adj pump near3 flip near2 flop	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/15 11:32
S231	487	charge adj pump with flip near2 flop	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/15 11:32
S230	487	charge adj pump with flip near flop	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/15 11:31

S229	33	charge adj pump with hold\$3 with up with down	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 17:24
S228	486	charge adj pump with hold	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 17:24
S227	330	((phase near3 detect\$4) same (flip near3 flop) same hold\$4	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 16:18
S226	2081	((phase near3 detect\$4) and (flip near3 flop) same hold\$4	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 16:18
S225	1	((phase near3 detect\$4) and (flip near3 flop) same stall near3 logic	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 15:44
S224	0	((phase near3 detect\$4) same (flip near3 flop) same stall near3 logic	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 15:43
S222	19	S221 not S220	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 15:17
S221	28	(((((variable adjustable) near3 (height resolution size) near2 phase)) and (@ad<"20030930") and (pll cdr dll clock)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 15:17
S220	9	(((((variable adjustable) near3 (height size) near2 phase)) and (@ad<"20030930") and (pll cdr dll clock)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 15:03

S219	65	((variable adjustable) near3 (height step size) near2 phase)) and (@ad<"20030930") and (pll cdr dll clock)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 14:49
S218	16	((phase clock pll dll cdr)) and (((variable adjustable) near3 height near2 phase)) and (@ad<"20030930")	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 14:47
S217	1	((phase near2 detect\$3)) and (((variable adjustable) near3 height near2 phase)) and (@ad<"20030930")	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 14:46
S216	23	S215 not S214	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 14:43
S215	39	((phase near2 detect\$3)) and ((variable near3 step near2 phase)) and (@ad<"20030930")	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 14:43
S214	26	((phase near2 detect\$3)) and ((variable near3 step near2 phase)) and (pll clock cdr dll)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 14:32
S213	60	((phase near2 detect\$3)) and ((variable near3 step near2 phase))	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 14:25
S212	7	((phase near2 detect\$3)) and ((step near2 phase near3 adjustable))	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 14:13
S211	382	((phase near2 detect\$3)) and ((step near2 phase near3 adjust\$4))	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 14:09

S210	1	11/976185	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 11:43
S209	32	S207 same (flip near3 flop)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 11:31
S208	476	S207 and (logic)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 11:30
S207	980	phase near3 detect\$4 same (phase near2 adj\$6) same (saml\$4)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 11:30
S206	135	{{(adjustable dynamic\$4 variable adj2 ((step step-size))) with (phase)) and (PLL clock data DLL CDR) and (@ack"20030930")	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 10:51
S205	234	{{(adjustable dynamic\$4 variable adj2 ((step step-size))) with (phase)) and (PLL clock data DLL CDR)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 10:50
S204	234	{{(adjustable dynamic\$4 variable adj2 ((step step-size))) with (phase)) and (PLL clock data DLL CDR)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 10:50
S203	329	{{(adjustable dynamic\$4 variable adj2 ((step step-size))) with (phase)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 10:50
S202	740	{{(adjustable dynamic\$4 variable adj2 ((step step-size))) same (phase)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 10:50

S200	24	S198 not S193	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 10:45
S198	26	((adjustable dynamic\$4 variable) adj2 ((step step-size))) same (phase and clock and data)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 10:44
S196	46	S195 not S193	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 10:36
S195	48	((adjustable dynamic\$4 variable) adj2 (phase adj2 (step step-size)))	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 10:36
S194	509	((adjust\$4 dynamic\$4 variable) near5 (phase near4 (step step-size))) same (clock)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 10:36
S193	26	((adjust\$4 dynamic\$4 variable) near5 (phase near4 (step step-size))) same (clock) same (rate)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/14 10:28
S192	2	(adjust\$4 near4 phase near3 (step size)) same ((data near3 rate) with (vary\$4 variable))	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/13 16:42
S191	4	(adjust\$4 near4 phase near3 (step size)) same (clock) and ((data adj rate) with (vary\$4 variable))	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/13 16:37
S190	103	(adjust\$4 near4 phase near3 (step size)) same (clock) and (data adj rate)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/13 16:13

S189	6	("20020131539" "20030212930" "6114914" "6901126" "7079615" "7162002").PN OR ("7315596").UPPN.	US-PGPUB; USPAT; USOCR	OR	ON	2009/01/13 16:12
S188	3	(adjust\$4 near4 phase near3 (step size)) same (clock) same data adj rate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 16:10
S187	450	(adjust\$4 near4 phase near3 (step size)) same (clock)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 16:10
S186	2791	(adjust\$4 near4 phase near3 (step size))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 16:09
S185	5	(phase near4 detect\$4) same sampl\$4 same ((variable vary\$4) near4 (data adj rate))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 16:07
S184	0	(phase near4 detect\$4) same ((phase step) near4 adjust\$6) same sampl\$4 same ((variable vary\$4) near4 (data adj rate))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/13 16:07
S183	4	(375/354,355,359,356,360,362,371,373,375,376.cds. 327/126,91,94,141,144,145,146,147,148,151,155,156,157,160,163.cds.) and (((averag\$3) near4 (up and down)) same ((phase near4 adjust\$4) (adjust\$4 near3 step\$1 near2 size)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/05 15:40
S182	12	(var\$5 near2 (data bit) near3 rate) same (phase near3 (step adjust\$4)) and (clock pll dll)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/05 15:36
S181	6	(var\$5 near2 (data bit) near3 rate) same (sampl\$4) same (phase near3 (step adjust\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/01/05 15:35

S180	2	(((adjust\$4 near4 (step\$1size (step adj size))) (staircase)) near5 phase) same (var\$5 near2 data bit) near3 rate)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/05 15:18
S179	1	(((adjust\$4 var\$5) near4 (step\$1size (step adj size))) (staircase)) near5 phase) and (pli dll) and (var\$5 near2 data near3 rate)	US-PGRUB; USPAT; EPO; JPO; DERWENT; BM_TDB	OR	ON	2009/01/05 15:16

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